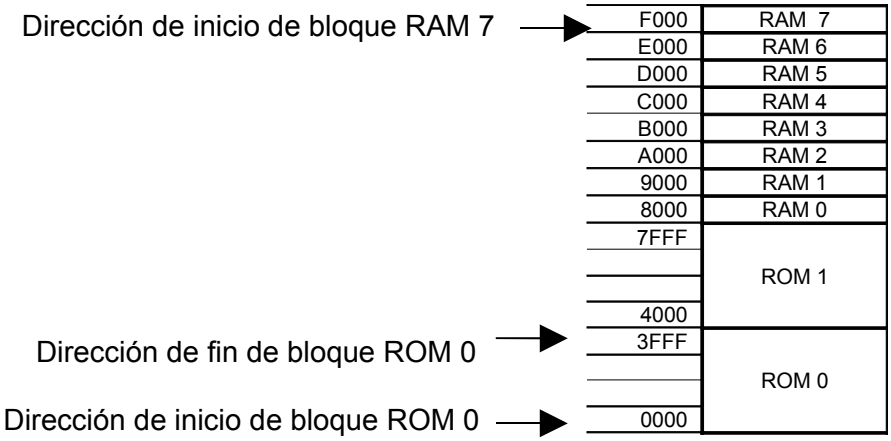
Practico 4
Memorias

Ejercicio 1

Se usaran 8 chips de memoria RAM y 2 chips de ROM.

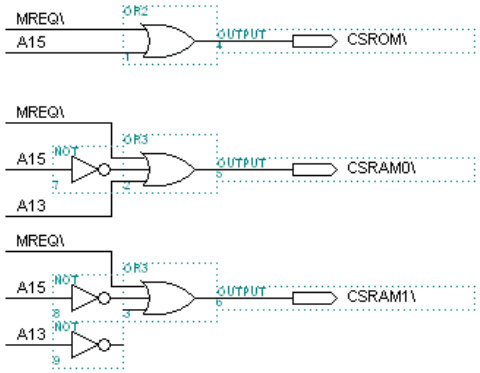


Ejercicio 2

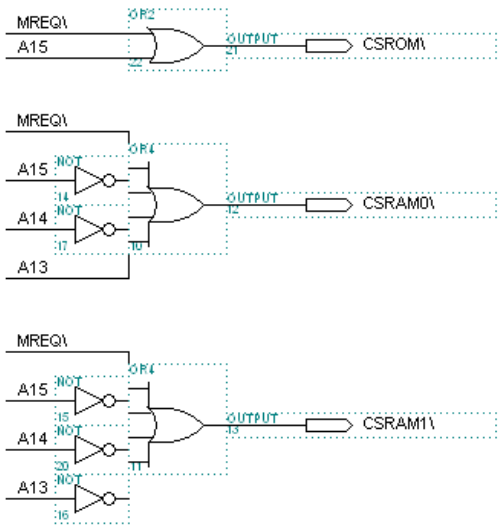
E000	CS07
C000	CS06
A000	CS05
8000	CS04
6000	CS03
4000	CS02
2000	CS01
0000	CS00

Ejercicio 3

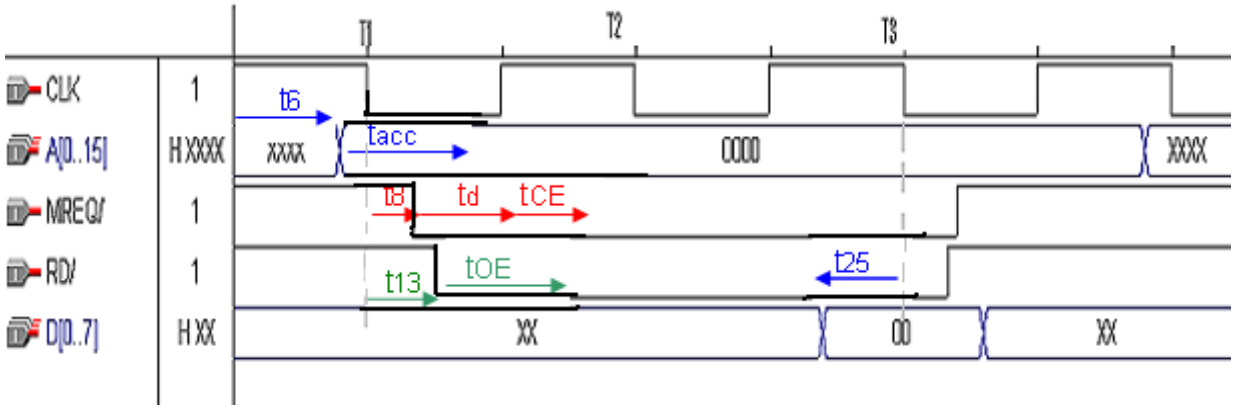
Caso 1



Caso 2



Ejercicio 5



datos de letra:
T = 162ns
T_{d_max} = 30ns

datos de cartilla del Z80:
t_{6_max} = 90ns
t_{13_max} = 80ns
t_{8_max} = 70ns
t_{25_min} = 40ns

1) $T_1 + T_2 + T_3/2 > t_{6\_max} + t_{acc\_max} + t_{25\_min}$ → $t_{acc\_max} < 275ns$
2) $T_1/2 + T_2 + T_3/2 > t_{13\_max} + t_{OE\_max} + t_{25\_min}$ → $t_{OE\_max} < 204ns$
3) $T_1/2 + T_2 + T_3/2 > t_{8\_max} + t_{d\_max} + t_{CE\_max} + t_{25\_min}$ → $t_{CE\_max} < 184ns$
   $T_1 + T_2 + T_3/2 > t_{6\_max} + t_{d\_max} + t_{CE\_max} + t_{25\_min}$ → $t_{CE\_max} < 245ns$

La memoria más lenta de la tabla, que cumple con todas las restricciones es: 27128-1.