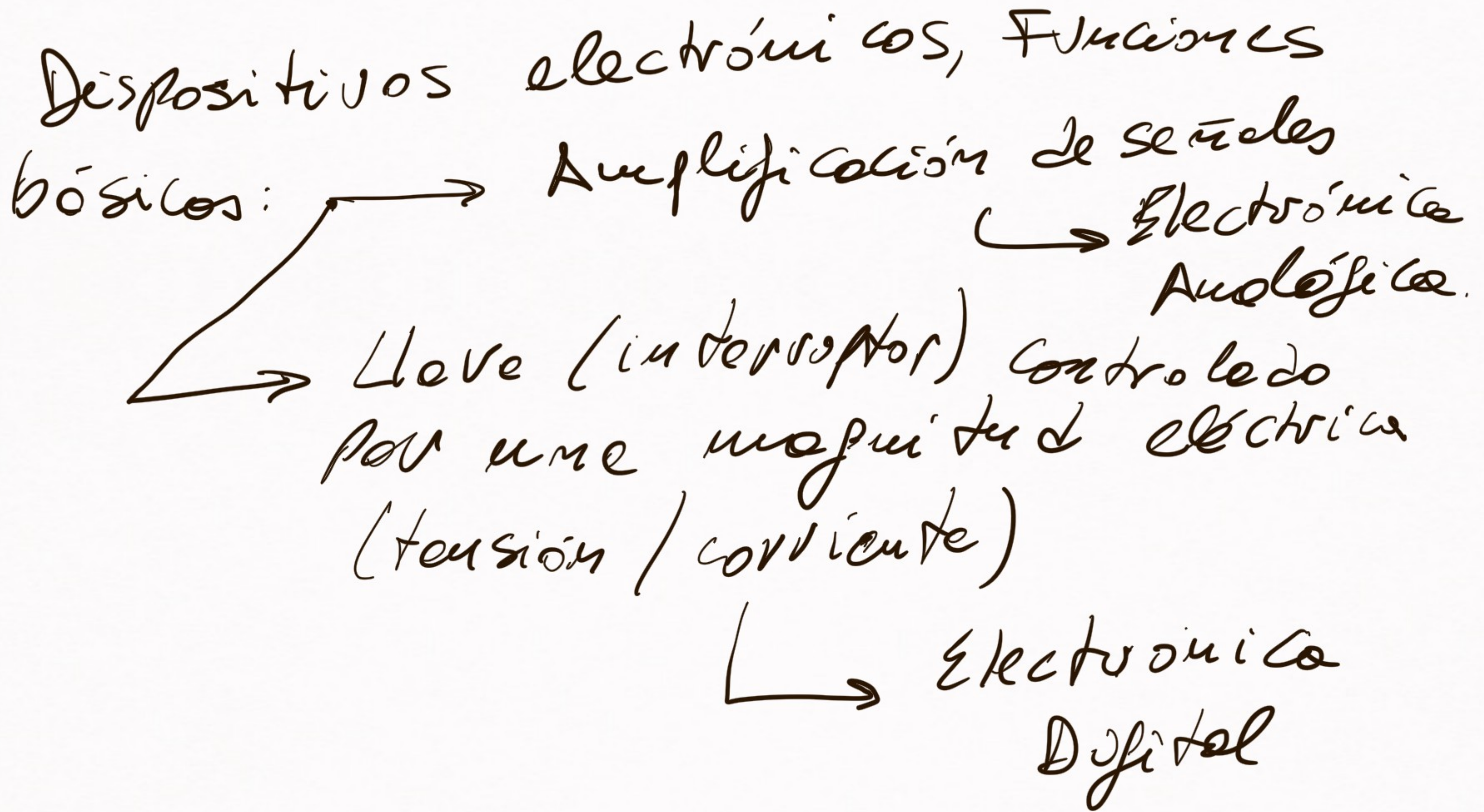


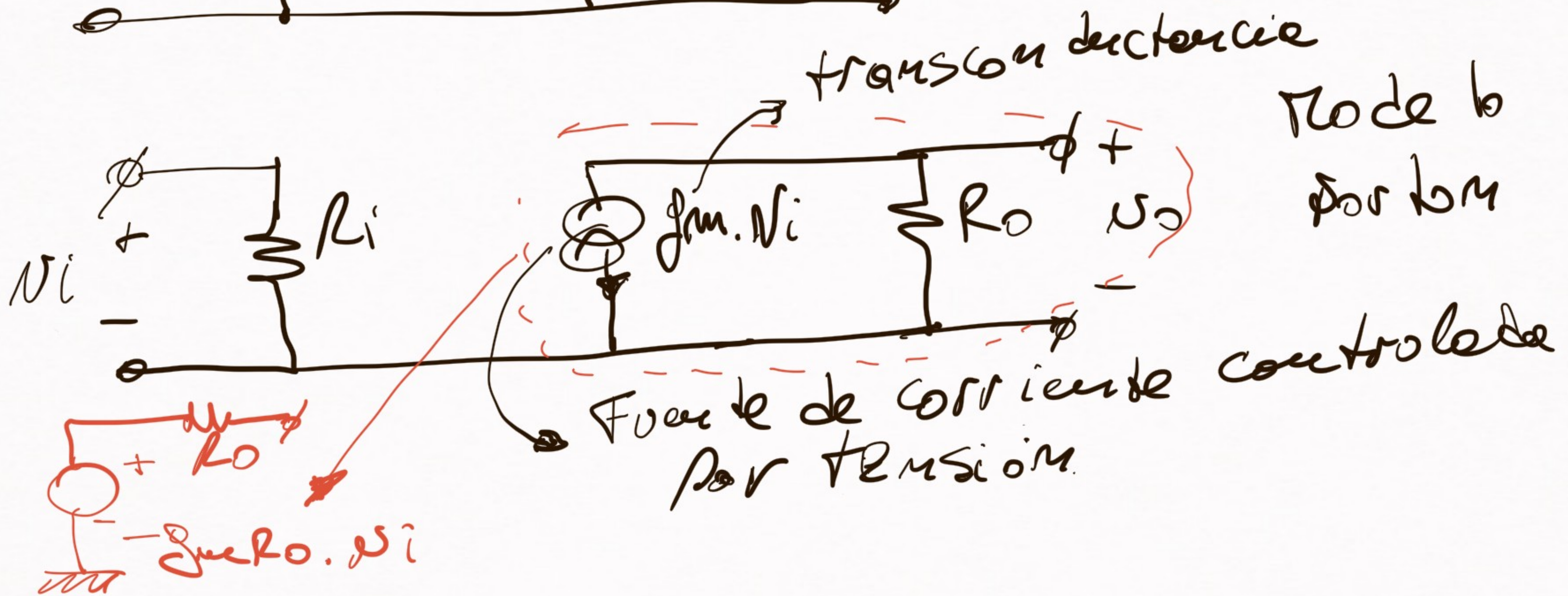
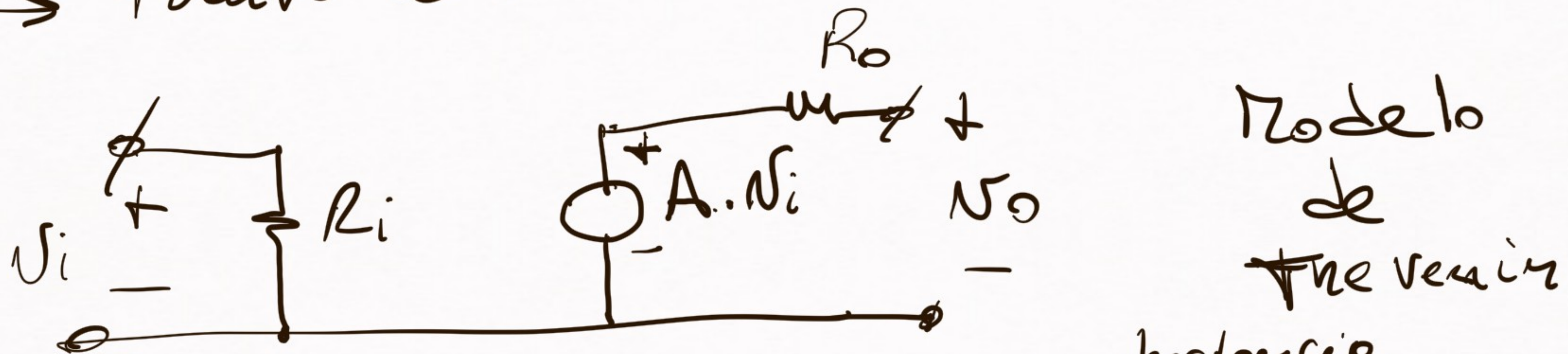
TRANSISTORES

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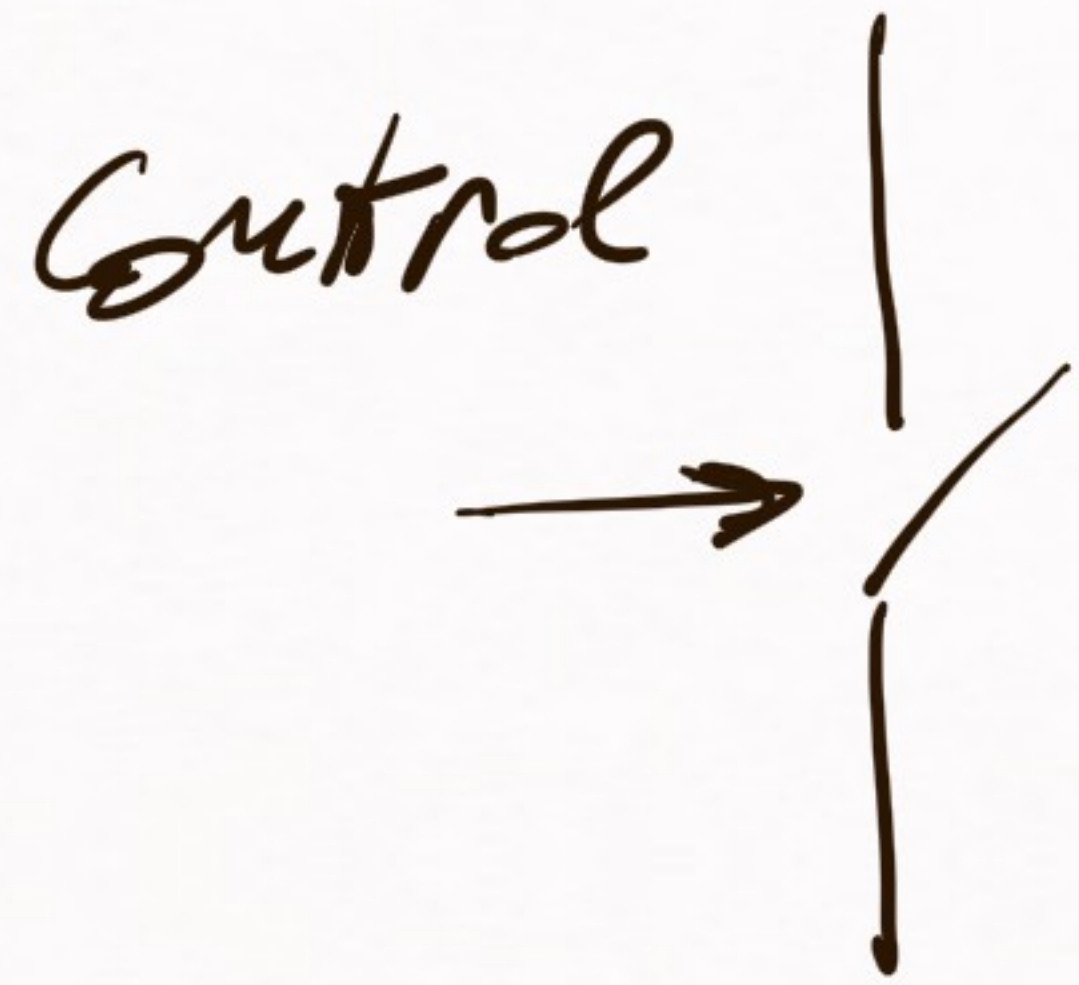


Amplificación de Señal

→ Fuente de tensión controlada por tensión



Llave controlada



Control	Llave
0	abierta
1	Cerrada



$$OUT = \overline{A \cdot B}$$

⇒ complemento de $A \cdot B$

