

Electrónica de potencia

Examen 22/02/2017

- El examen dura **4 horas**.
- Está permitido el uso de **una** hoja de formulas y calculadora.
- Entregar los problemas en **hojas separadas**, numeradas (indicando total), con nombre y C.I.

Problema 1

Se tiene un puente seis pulsos – dos vías que está conectado a una máquina DC mediante una inductancia L , y a la red de trifásica de $230 Vca \pm 20\%$, $50 Hz$ mediante un transformador de impedancia de cortocircuito $1j \Omega$ y relación de transformación unitaria. La tensión de armadura se mantendrá fija $230 Vcc$ (magnitud sin signo). El propósito del sistema es inyectar a la red de alterna toda la potencia que una máquina de arrastre le transmite en el eje a la máquina DC.

El puente se construye con tiristores CD431290 cuya hoja de datos se adjunta.

- a) Determinar la máxima potencia que se podrá inyectar a la red sin que el puente entre en falla de conmutación, para toda condición de la red. Para esta parte puede despreocuparse de las pérdidas de energía en el puente. **Indicar** para qué tensión de red el puente trabajará en el límite antes de que se produzca la falla de conmutación (para la potencia determinada).
- b) Para plena carga y la tensión de red indicada en a), dibujar:
 - Tensión en bornes de continua del puente.
 - Corriente de una fase del secundario del transformador.
 - Tensión de un borne de alterna del puente, respecto al neutro del sistema.

Indicar amplitudes, ángulos relevantes y en el caso de la corriente dibujar con las concavidades adecuadas. Suponer que no se entra en falla de conmutación.

- c) Calcular L para asegurar conducción continua con potencias mayores al 10% de la máxima determinada en la parte a), independientemente de la variación de la red.
- d) Si se coloca cada módulo en un disipador separado, determinar la resistencia térmica máxima que puede tener cada uno de los mismos para no superar una temperatura media de juntura de $110^\circ C$, teniendo una temperatura ambiente máxima de $40^\circ C$.

Notas:

- Para las partes b) y d) se podrá asumir la corriente continua lisa.
- Para la parte c) se podrá despreocuparse de la inductancia del transformador.
- En la hoja de datos $t_{off} = t_q$.

Problema 2

Se tiene un convertidor *forward* con una carga en 24 V que demanda como máximo 5 A . El mismo se alimenta de un banco de baterías cuya tensión puede variar entre 45 V y 55 V . La frecuencia de conmutación será de 50 kHz .

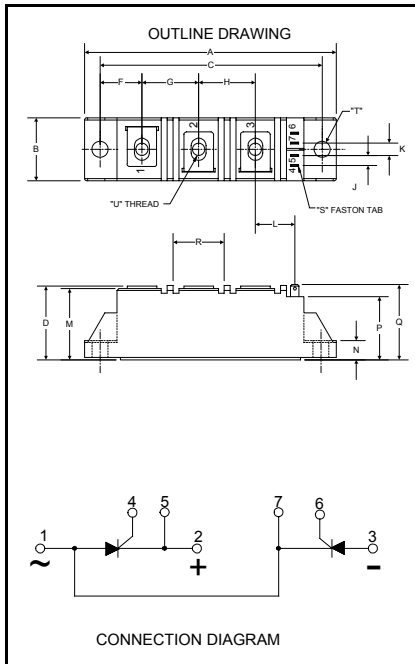
La cantidad de vueltas del primario y secundario es la misma, y se sabe que el núcleo disponible tiene una inductancia específica de $A_L = 1,2 \frac{\mu\text{H}}{\text{vueltas}^2}$, y un flujo máximo de $26,6\ \mu\text{Wb}$.

Podrán considerarse ideales a todos los dispositivos de conmutación.

- a) Determinar la inductancia del filtro de salida para tener un ripple de corriente no mayor a 1 App .
- b) Calcular el valor mínimo para $N_p = N_s$ de modo de no superar el flujo máximo.
- c) Determinar las vueltas del bobinado de desmagnetización para un correcto funcionamiento.
- d) Con el fin de seleccionar el transistor necesario, indicar qué tensión y corriente instantáneas máximas deberá soportar la llave.
- e) Seleccionar un condensador (o un conjunto de ellos) del catálogo adjunto para acotar el ripple de tensión de salida al 2% .

Powerex, Inc., Hillis Street, Youngwood, Pennsylvania 15697 (724) 925-7272

POW-R-BLOK™
Dual SCR Isolated Module
90 Amperes / Up to 1600 Volts



CD43_90
Dual SCR Isolated
POW-R-BLOK™ Module
 90 Amperes / Up to 1600 Volts

Description:

Powerex Dual SCR Modules are designed for use in applications requiring phase control and isolated packaging. The modules are isolated for easy mounting with other components on a common heatsink. *POW-R-BLOK™* has been tested and recognized by the Underwriters Laboratories.

Features:

- Electrically Isolated Heatsinking
- DBC Alumina (Al₂O₃) Insulator
- Glass Passivated Chips
- DBC Alumina (Al₂O₃) Baseplate
- Low Thermal Impedance for Improved Current Capability
- UL Recognized (E78240)

Benefits:

- No Additional Insulation Components Required
- Easy Installation
- No Clamping Components Required
- Reduce Engineering Time

Applications:

- Bridge Circuits
- AC & DC Motor Drives
- Battery Supplies
- Power Supplies
- Large IGBT Circuit Front Ends
- Lighting Control
- Heat & Temperature Control
- Welders

CD43 Outline Dimensions

Dimension	Inches	Millimeters
A	3.62	92
B	0.81	20.5
C	3.15	80
D	1.18	30
F	0.59	15
G	0.79	20
H	0.79	20
J	0.16	4
K	0.23	5.8
L	0.61	15.5
M	1.14	29
N	0.24	6.1
P	0.94	24
Q	1.18	30
R	0.71	18
S	0.11 x .03	2.8 x 0.8
T	0.25	6.3
U	M5	M5

Note: Dimensions are for reference only

Ordering Information:

Select the complete eight digit module part number from the table below. Example: CD431690 is a 1600Volt, 90 Ampere Dual SCR Isolated *POW-R-BLOK™* Module

Type	Voltage Volts (x100)	Current Amperes (x 1)
CD43	08 12 16	90

Absolute Maximum Ratings

Characteristics	Conditions	Symbol	Units
Repetitive Peak Forward and Reverse Blocking Voltage		V_{DRM} & V_{RRM}	up to 1600 V
Non-Repetitive Peak Reverse Blocking Voltage ($t < 5$ msec)		V_{RSM}	$V_{RRM} + 100$ V
RMS Forward Current	180° Conduction, $T_C=87^\circ\text{C}$	$I_{T(RMS)}$	140 A
	180° Conduction, $T_C=87^\circ\text{C}$ (AC Switch)	$I_{T(RMS)}$	200 A
Average Forward Current	180° Conduction, $T_C=87^\circ\text{C}$	$I_{T(AV)}$	90 A
Peak One Cycle Surge Current, Non-Repetitive	60 Hz, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	1570 A
	60 Hz, No V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	1870 A
	60 Hz, No V_{RRM} reapplied, $T_j=25^\circ\text{C}$	I_{TSM}	2100 A
	50 Hz, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	1500 A
	50 Hz, No V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	1785 A
	50 Hz, No V_{RRM} reapplied, $T_j=25^\circ\text{C}$	I_{TSM}	2000 A
Peak Three Cycle Surge Current, Non-Repetitive	60 Hz, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	1210 A
	50 Hz, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	1155 A
Peak Ten Cycle Surge Current, Non-Repetitive	60 Hz, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	960 A
	50 Hz, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I_{TSM}	940 A
I^2t for Fusing for One Cycle, 8.3 milliseconds	8.3 ms, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I^2t	10,270 A^2sec
	8.3 ms, No V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I^2t	14,520 A^2sec
	8.3 ms, No V_{RRM} reapplied, $T_j=25^\circ\text{C}$	I^2t	18,300 A^2sec
	10 ms, 100% V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I^2t	11,250 A^2sec
	10 ms, No V_{RRM} reapplied, $T_j=125^\circ\text{C}$	I^2t	15,910 A^2sec
	10 ms, No V_{RRM} reapplied, $T_j=25^\circ\text{C}$	I^2t	20,000 A^2sec
Maximum Rate-of-Rise of On-State Current, (Non-Repetitive)	$T_j=25^\circ\text{C}$, $I_G=0.5$ A, $V_D=0.67 V_{DRM}$ (Rated), $I_{TM}=300\text{A}$, $T_r < 0.5\mu\text{s}$, $t_p > 6\mu\text{s}$	di/dt	150 A/ μs
Peak Gate Power Dissipation	$T_p < 5$ ms, $T_j = 125^\circ\text{C}$	P_{GM}	12 W
Average Gate Power Dissipation	$F = 50$ Hz, $T_j = 125^\circ\text{C}$	$P_{G(AV)}$	3 W
Peak Forward Gate Current	$T_p < 5$ ms, $T_j = 125^\circ\text{C}$	I_{GFM}	3 A
Peak Reverse Gate Voltage	$T_p < 5$ ms, $T_j = 125^\circ\text{C}$	V_{GRM}	10 V
Operating Temperature		T_J	-40 to +125 °C
Storage Temperature		T_{stg}	-40 to +125 °C
Max. Mounting Torque, M5 Mounting Screw on Terminals			25 in.-Lb.
			3 Nm
Max. Mounting Torque, Module to Heatsink			44 in.-Lb.
			5 Nm
Module Weight, Typical			83 g
			3 oz.
V Isolation @ 25C	50 – 60 Hz, 1 minute	V_{rms}	2500 V
Circuit to base, all terminals shorted together	50 – 60 Hz, 1 second	V_{rms}	3500 V

Electrical Characteristics, T_J=25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Max.	Units
Repetitive Peak Forward Leakage Current	I _{DRM}	Up to 1600V, T _J =125°C		15	mA
Repetitive Peak Reverse Leakage Current	I _{RDM}	Up to 1600V, T _J =125°C		15	mA
Peak On-State Voltage	V _{TM} / V _{FM}	I _{TM} / I _{FM} = 300A		1.58	V
Threshold Voltage, Low-level	V _{(TO)1}	T _J = 125°C, I = 16.7% x πI _{T(AV)} to πI _{T(AV)}		0.80	V
Slope Resistance, Low-level	r _{T1}			2.40	mΩ
Threshold Voltage, High-level	V _{(TO)2}	T _J = 125°C, I = πI _{T(AV)} to I _{TSM}		0.85	V
Slope Resistance, High-level	r _{T2}			2.25	mΩ
V _{TM} Coefficients, Full Range		T _J = 125°C, I = 15% x I _{T(AV)} to I _{TSM} V _{TM} = A + B Ln I + C I + D Sqrt I	A = B = C = D =	0.7160 2.17E-02 2.20E-03 1.58E-03	
Minimum dV/dt	dV/dt	Linear to 2/3 V _{DRM} T _J =125°C, Gate Open Circuit	500		V/μs
Turn-Off Time (Typical)	t _{off}	T _J = 25°C, I _T = 2A V _r = 50V, -di/dt=10 A/μs Re-Applied dV/dt = 200 V/μs, Linear to 900 V	40 - 100	(Typical)	μs
Gate Trigger Current	I _{GT}	T _J = -40°C, V _D =6V, Resistive Load T _J = 25°C, V _D =6V, Resistive Load T _J =125°C, V _D =6V, Resistive Load		270 150 80	mA mA mA
Gate Trigger Voltage	V _{GT}	T _J = -40°C, V _D =6V, Resistive Load T _J = 25°C, V _D =6V, Resistive Load T _J =125°C, V _D =6V, Resistive Load		4.0 2.5 1.7	Volts Volts Volts
Non-Triggering Gate Voltage	V _{GDM}	T _J =125°C, V _D =V _{DRM}		0.25	Volts
Non-Triggering Gate Current	I _{GDM}	T _J =125°C, V _D =V _{DRM}		6	mA
Holding Current	I _H	V _D =6V, Resistive Load, Gate Open		200	mA
Latching Current	I _L	V _D =6V, Resistive Load		400	mA

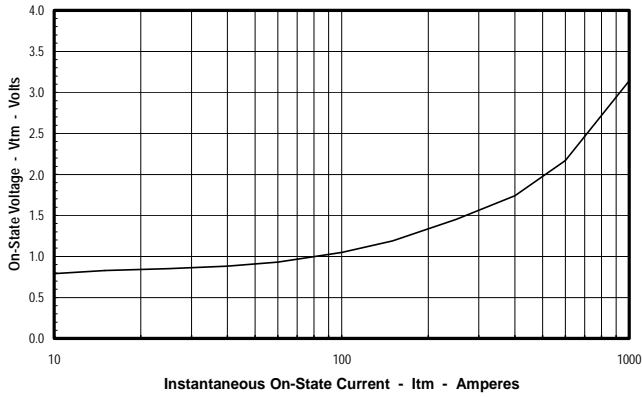
Thermal Characteristics

Characteristics	Symbol		Max.	Units
Thermal Resistance, Junction to Case DC Operation	R _{ΘJ-C}	Per Module, both conducting Per Junction, both conducting	0.135 0.270	°C/W °C/W
Thermal Impedance Coefficients	Z _{ΘJ-C}	Z _{ΘJ-C} = K ₁ (1-exp(-t/τ ₁)) + K ₂ (1-exp(-t/τ ₂)) + K ₃ (1-exp(-t/τ ₃)) + K ₄ (1-exp(-t/τ ₄))	K ₁ = 6.48 E-3 K ₂ = 6.02 E-2 K ₃ = 1.64 E-1 K ₄ = 3.94 E-2	τ ₁ = 5.80 E-4 τ ₂ = 1.70 E-2 τ ₃ = 9.54 E-2 τ ₄ = 3.53 E-1
Thermal Resistance, Case to Sink Lubricated	R _{ΘC-S}	Per Module	0.1	°C/W

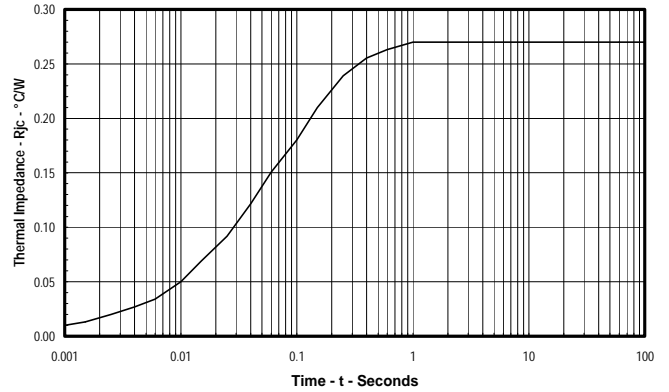
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POW-R-BLOK™ Dual SCR Isolated Module 90 Amperes / Up to 1600 Volts

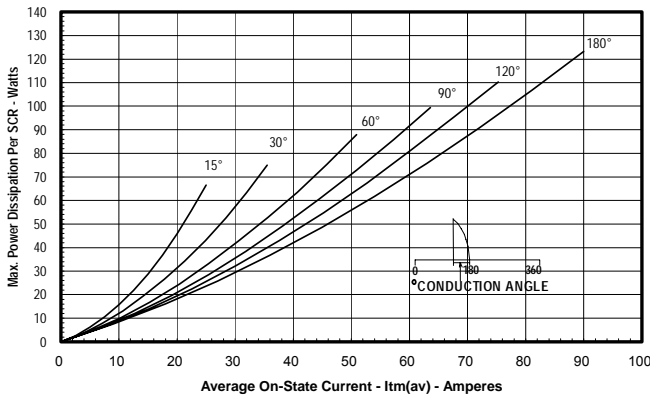
Maximum On-State Forward Voltage Drop
($T_j = 125^\circ\text{C}$)



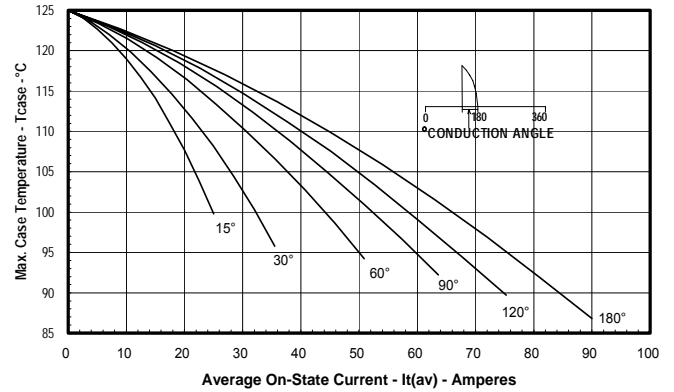
Maximum Transient Thermal Impedance
(Junction to Case)



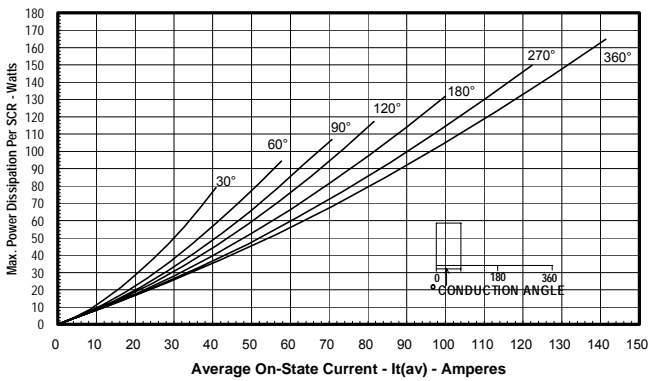
Maximum On-State Power Dissipation
(Sinusoidal Waveform)



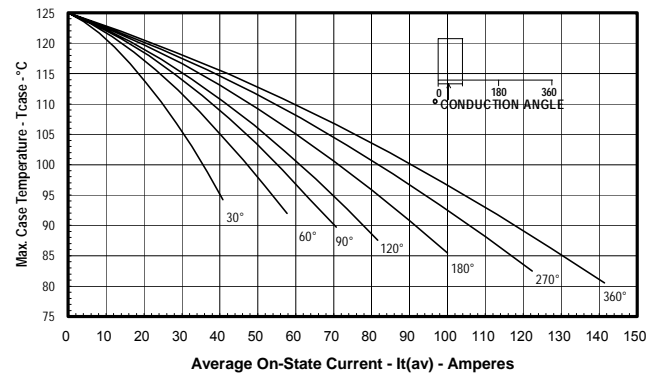
Maximum Allowable Case Temperature
(Sinusoidal Waveform)

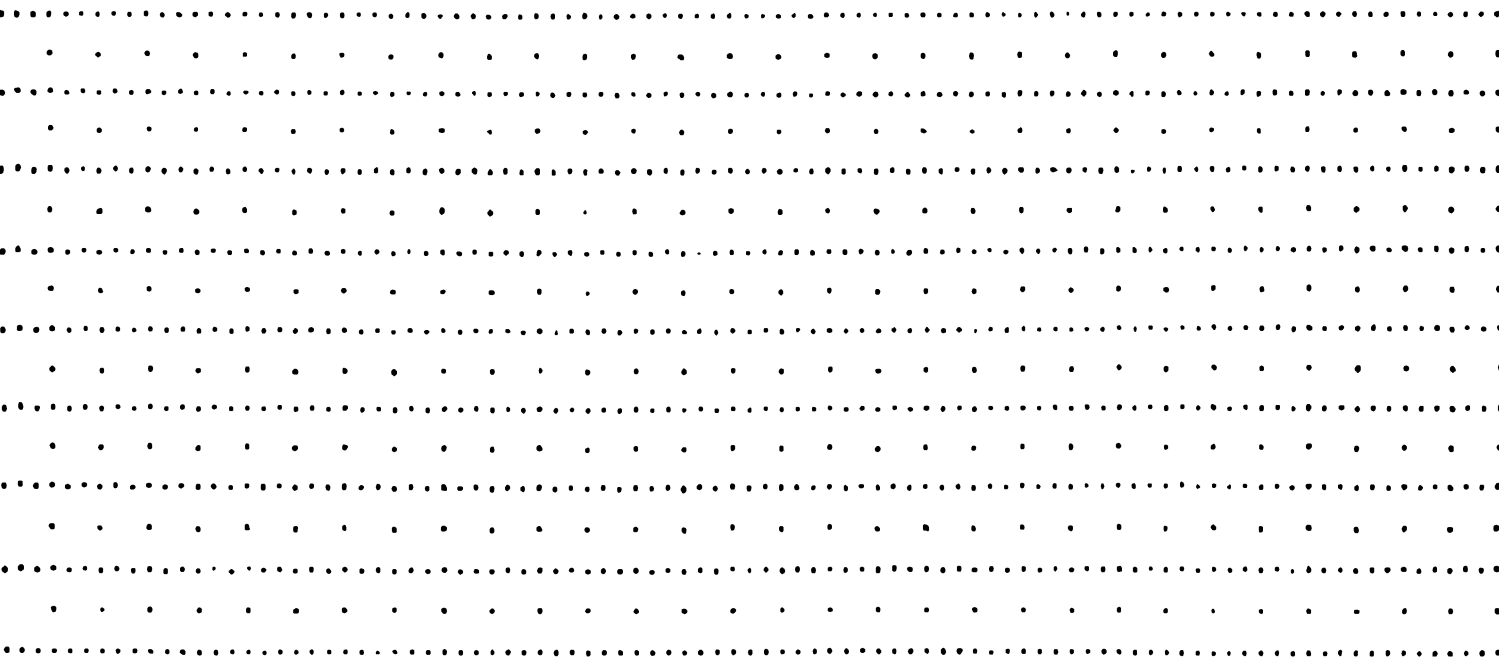
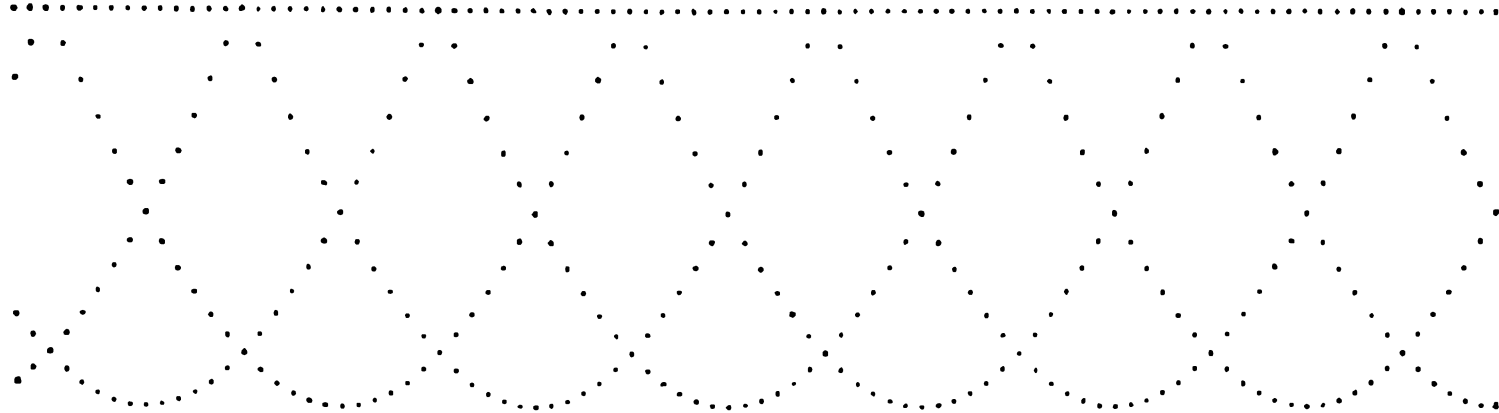
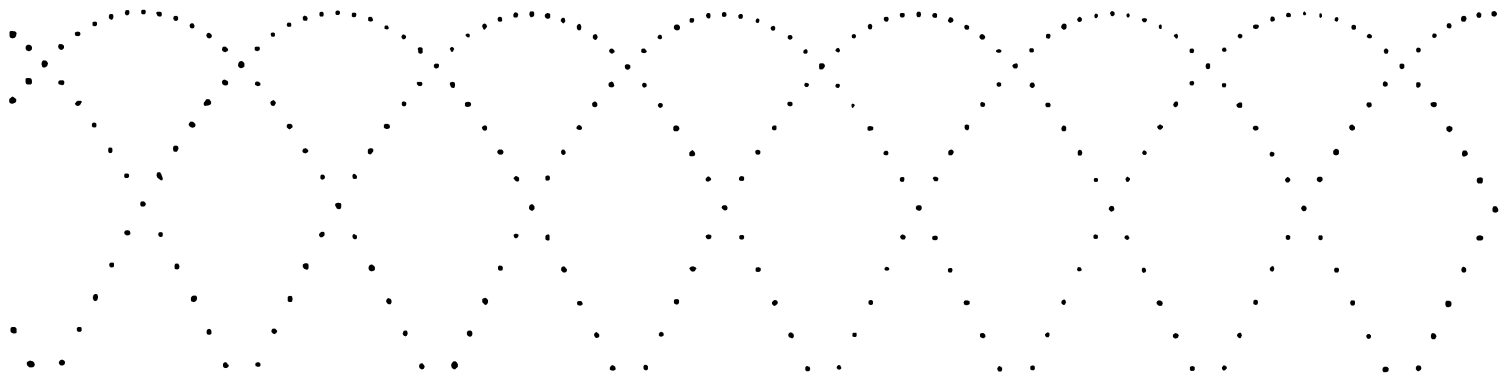
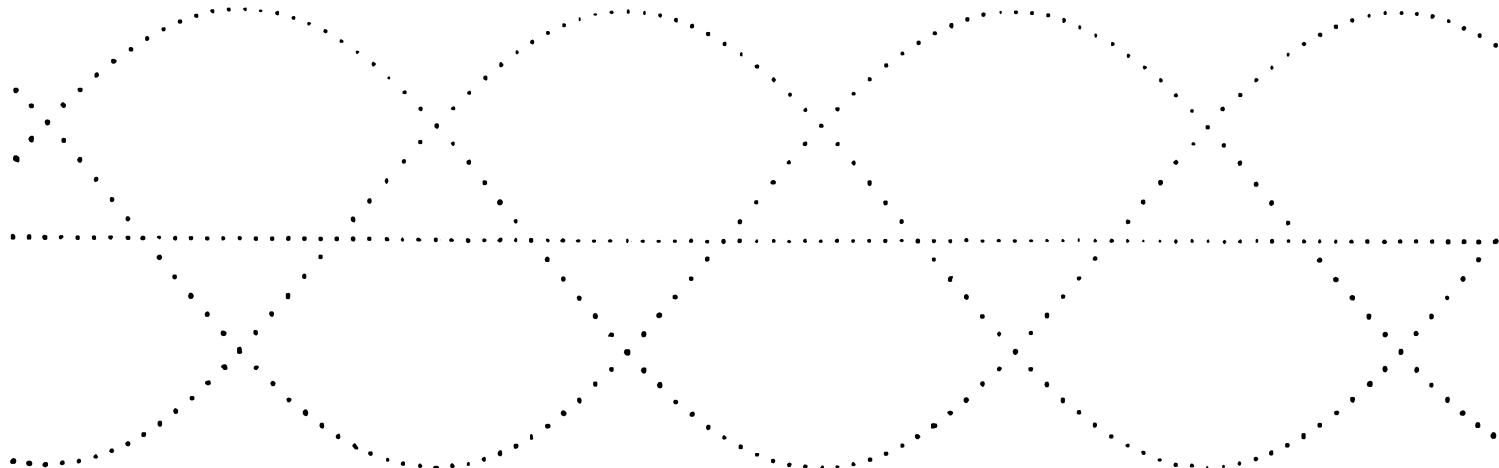


Maximum On-State Power Dissipation
(Rectangular Waveform)



Maximum Allowable Case Temperature
(Rectangular Waveform)






B41866
High ripple current – 125 °C
Technical data and ordering codes

C_R 120 Hz 20 °C μF	Case dimensions $d \times l$ mm	ESR_{max} 10 kHz –40 °C Ω	ESR_{max} 10 kHz 20 °C Ω	Z_{max} 100 kHz 20 °C Ω	$I_{\text{AC,R}}$ 100 kHz 125 °C mA	Ordering code (composition see below)
$V_R = 25 \text{ V DC}$						
100	8 × 11.5	5.170	0.646	0.573	297	B41866C5107M***
120	8 × 11.5	5.170	0.646	0.573	297	B41866C5127M***
150	8 × 11.5	5.170	0.646	0.573	297	B41866C5157M***
180	8 × 11.5	5.170	0.646	0.573	297	B41866C5187M***
220	8 × 11.5	5.170	0.646	0.573	297	B41866C5227M***
270	10 × 12.5	2.980	0.373	0.336	450	B41866C5277M***
330	10 × 12.5	2.980	0.373	0.336	450	B41866C5337M***
390	10 × 16	1.404	0.175	0.160	714	B41866C5397M***
470	10 × 16	1.404	0.175	0.160	714	B41866C5477M***
560	10 × 20	1.070	0.134	0.127	875	B41866C5567M***
680	10 × 20	1.070	0.134	0.127	875	B41866C5687M***
820	12.5 × 20	0.881	0.110	0.104	1105	B41866C5827M***
1000	12.5 × 20	0.881	0.110	0.104	1105	B41866C5108M***
1200	12.5 × 25	0.710	0.089	0.082	1358	B41866C5128M***
1500	12.5 × 25	0.710	0.089	0.082	1358	B41866C5158M***
1800	16 × 20	0.401	0.050	0.046	1895	B41866C5188M***
2200	12.5 × 40	0.406	0.051	0.047	2185	B41866C5228M***
2200	16 × 25	0.314	0.039	0.037	2279	B41866D5228M***
2200	18 × 20	0.341	0.043	0.040	2190	B41866E5228M***
2700	18 × 25	0.312	0.039	0.037	2454	B41866D5278M***
3300	16 × 31.5	0.248	0.031	0.029	2822	B41866D5338M***
3900	16 × 35.5	0.200	0.025	0.024	3230	B41866E5398M***
3900	18 × 31.5	0.224	0.028	0.027	3178	B41866D5398M***
4700	18 × 35	0.184	0.023	0.022	3638	B41866D5478M***
5600	18 × 40	0.152	0.019	0.018	4244	B41866C5568M***

Composition of ordering code

*** = Version

000 = for standard leads, bulk

 001 = for kinked leads, bulk (for $d \times l = 10 \times 20 \dots 12.5 \times 25 \text{ mm}$ and $\varnothing 16 \dots 18 \text{ mm}$)

 002 = for cut leads, bulk (for $\varnothing 10 \dots 18 \text{ mm}$, excluding $d \times l = 12.5 \times 40 \text{ mm}$)

 003 = for crimped leads, blister (for $\varnothing 16 \dots 18 \text{ mm}$)

 004 = for J leads, blister (for $\varnothing 10 \dots 18 \text{ mm}$, excluding $d \times l = 12.5 \times 40$ and $18 \times 40 \text{ mm}$)

 006 = for taped leads, Ammo pack, lead spacing $F = 3.5 \text{ mm}$ (for $\varnothing 8 \text{ mm}$)

 008 = for taped leads, Ammo pack, lead spacing $F = 5.0 \text{ mm}$ (for $d \times l = 8 \times 11.5 \dots 12.5 \times 25 \text{ mm}$)

 009 = for taped leads, Ammo pack, lead spacing $F = 7.5 \text{ mm}$ (for $d \times l = 16 \times 20 \dots 16 \times 31.5 \text{ mm}$ and $18 \times 20 \dots 18 \times 31.5 \text{ mm}$)

 012 = for bent 90° leads, blister (for $\varnothing 16 \dots 18 \text{ mm}$)


B41866
High ripple current – 125 °C
Technical data and ordering codes

C_R 120 Hz 20 °C μF	Case dimensions $d \times l$ mm	ESR_{max} 10 kHz –40 °C Ω	ESR_{max} 10 kHz 20 °C Ω	Z_{max} 100 kHz 20 °C Ω	$I_{\text{AC,R}}$ 100 kHz 125 °C mA	Ordering code (composition see below)
$V_R = 50 \text{ V DC}$						
47	8 × 11.5	5.687	0.711	0.631	370	B41866C6476M***
56	8 × 11.5	5.429	0.679	0.602	370	B41866C6566M***
68	8 × 11.5	5.170	0.646	0.573	370	B41866C6686M***
82	8 × 11.5	5.170	0.646	0.573	370	B41866C6826M***
100	10 × 12.5	2.980	0.373	0.336	450	B41866C6107M***
120	10 × 16	1.404	0.175	0.160	714	B41866C6127M***
150	10 × 16	1.404	0.175	0.160	714	B41866C6157M***
180	10 × 20	1.070	0.134	0.127	875	B41866C6187M***
220	10 × 20	1.070	0.134	0.127	875	B41866C6227M***
270	10 × 20	1.070	0.134	0.127	875	B41866C6277M***
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390	12.5 × 25	0.710	0.089	0.082	1358	B41866C6397M***
470	12.5 × 25	0.710	0.089	0.082	1358	B41866C6477M***
470	16 × 20	0.680	0.085	0.080	1370	B41866D6477M***
560	16 × 20	0.401	0.050	0.046	1895	B41866C6567M***
680	16 × 25	0.314	0.039	0.037	2279	B41866C6687M***
820	16 × 25	0.314	0.039	0.037	2279	B41866C6827M***
820	18 × 20	0.344	0.043	0.040	2190	B41866E6827M***
1000	16 × 31.5	0.249	0.031	0.029	2822	B41866C6108M***
1000	18 × 25	0.314	0.039	0.037	2454	B41866D6108M***
1200	16 × 35.5	0.200	0.025	0.024	3230	B41866D6128M***
1200	18 × 31.5	0.226	0.028	0.027	3178	B41866C6128M***
1500	18 × 35	0.187	0.023	0.022	3638	B41866C6158M***
1800	18 × 40	0.153	0.019	0.018	4244	B41866C6188M***

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- 002 = for cut leads, bulk (for $\varnothing 10 \dots 18$ mm, excluding $d \times l = 12.5 \times 40$ mm)
- 003 = for crimped leads, blister (for $\varnothing 16 \dots 18$ mm)
- 004 = for J leads, blister (for $\varnothing 10 \dots 18$ mm, excluding $d \times l = 12.5 \times 40$ and 18×40 mm)
- 006 = for taped leads, Ammo pack, lead spacing $F = 3.5$ mm (for $\varnothing 8$ mm)
- 008 = for taped leads, Ammo pack, lead spacing $F = 5.0$ mm (for $d \times l = 8 \times 11.5 \dots 12.5 \times 25$ mm)
- 009 = for taped leads, Ammo pack, lead spacing $F = 7.5$ mm (for $d \times l = 16 \times 20 \dots 16 \times 31.5$ mm and $18 \times 20 \dots 18 \times 31.5$ mm)
- 012 = for bent 90° leads, blister (for $\varnothing 16 \dots 18$ mm)

22/02/2017Problema 1

a) Para una tensión de red dada la corriente límite va a estar dada por

$$\delta = \delta_{\min} = \frac{100 \mu s}{20 \text{ms}} \cdot 360^\circ = 1,8^\circ \quad (t_g = 100 \mu s)$$

$$\Rightarrow -U_d = \frac{3}{\pi} \sqrt{2} U \cos \delta_{\min} - \frac{3}{\pi} X_{ce} I$$

$$\Rightarrow I = \frac{1}{X_{ce}} \cdot \left\{ \frac{\pi U_d}{3} + \sqrt{2} \cdot U \cdot \cos \delta_{\min} \right\}$$

El valor mínimo que corresponde a $U = U_{\min}$ es el que pueda asegurar para todo punto de operación.

$$I = 19,23 \text{ A} \quad (\text{con } U = 0,8 \cdot 230 \text{ V})$$

que corresponde a $P = 19,23 \text{ A} \cdot 230 \text{ V} = 4423 \text{ W}$

Con esta corriente máxima se estará en el límite de falla de conmutación

si $U = U_{\min}$

Problema 1 (cont.)

S.E. ②

$$b) \alpha = \cos^{-1} \left\{ (U_d + \frac{3}{\pi} X_{cu} I) \frac{\pi}{3\sqrt{2} U_{min}} \right\} = 148,4^\circ$$

$$\mu = 180^\circ - \alpha - \chi_{min} = 29,8^\circ$$

Ver papel trifásico.

c) El rizado de corriente se maximiza con el α menor, es decir con $U = U_{min}$.
Desprecia el efecto de la conmutación.

$$\cos \alpha_0 = \frac{U_d}{\frac{3}{\pi} \sqrt{2} U_{min}} \Rightarrow \alpha_0 = 123,1^\circ$$

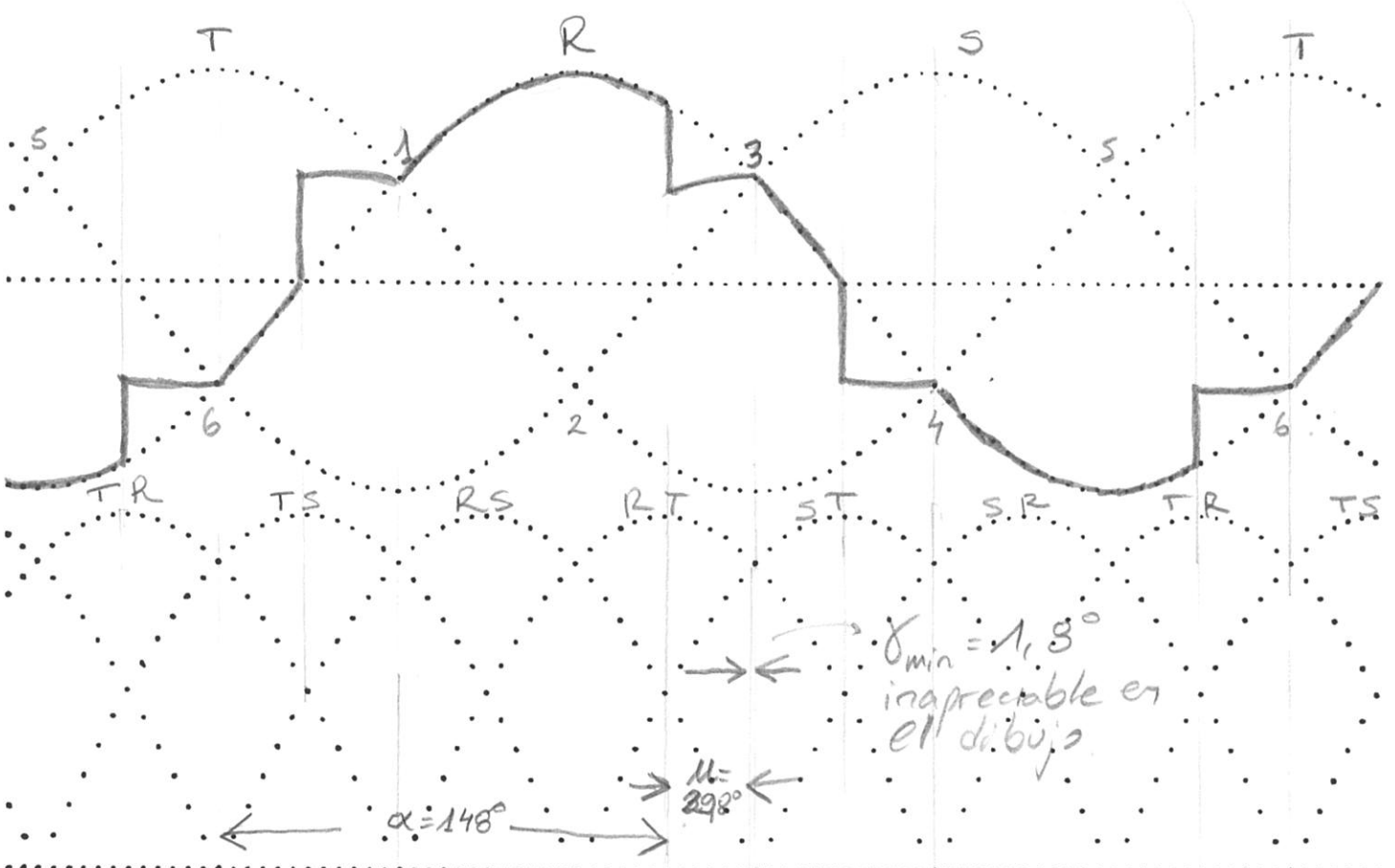
$$I_{L_{med}} \approx \frac{1}{3} \frac{U_{min} \alpha}{L \omega} \cdot \sin \alpha_0 < 1,923 A$$

$$\Rightarrow L > \frac{1}{3} \cdot \frac{1,2 \cdot 230 V}{1,923 A \cdot 2\pi \cdot 50 Hz} \cdot \sin 123,1^\circ = 44,94 mH$$

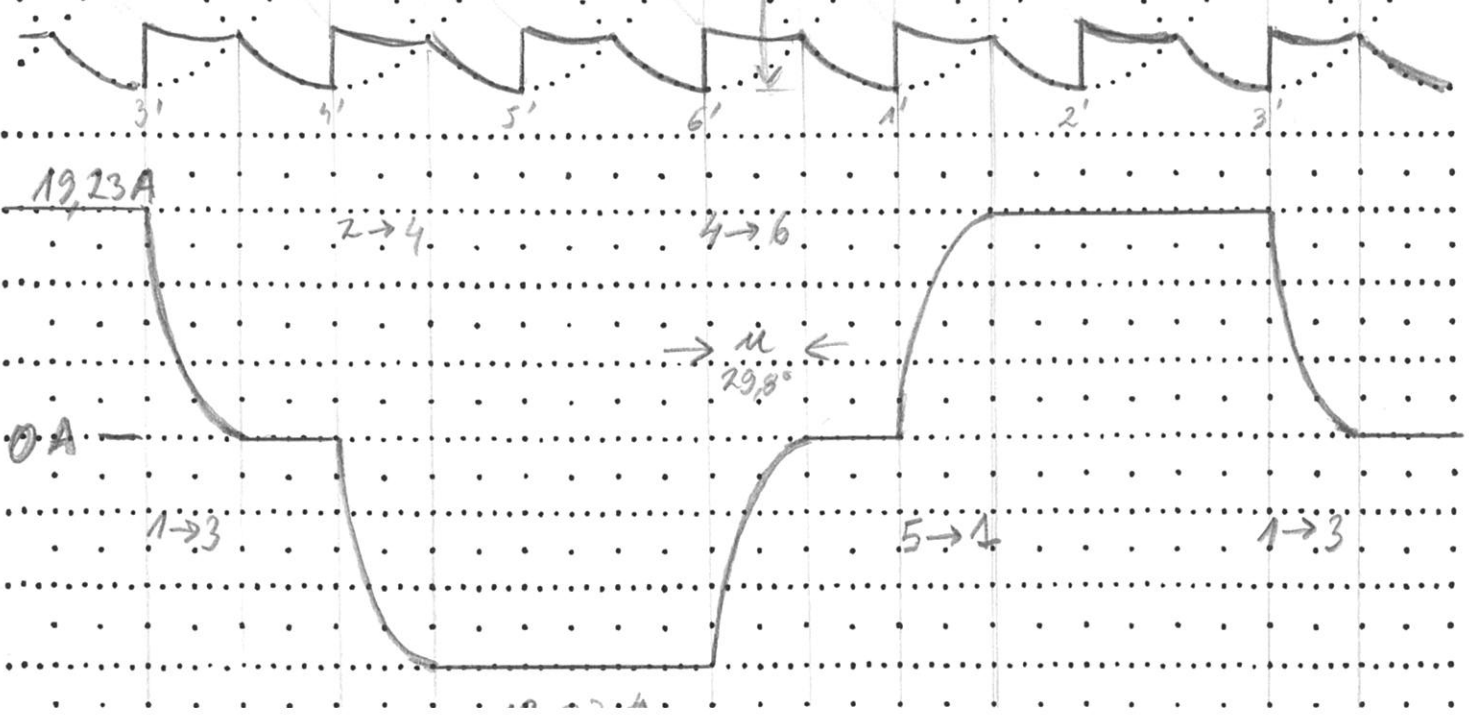
$$d) \langle P \rangle_{\text{por módulo}} = 2 \langle P \rangle_{\text{por tiristor}} = 2 \cdot \frac{1}{3} (r_T \cdot I + V_{T0}) \cdot I = 10,8 W$$

$$R_{sa} < \frac{110^\circ C - 40^\circ C}{10,8 W} - \overbrace{R_{jc}}^{0,135^\circ C/W} - \overbrace{R_{cs}}^{0,1^\circ C/W} = 6,217^\circ C/W$$

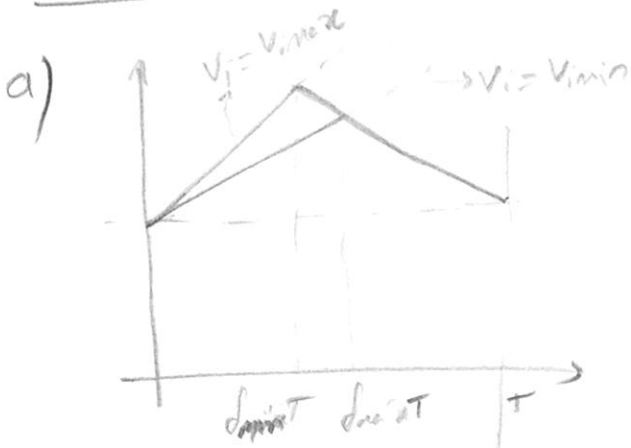
por módulo por módulo



$$\sqrt{2} \cdot 230 \cdot 0.8 = 260V$$



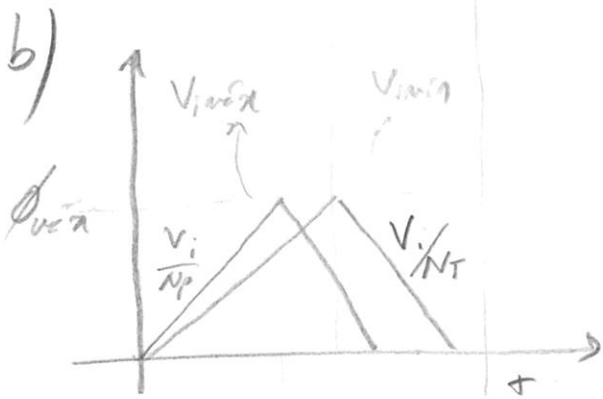
Problema 2



$$\Delta I_L = \frac{V_i - V_o}{L} \cdot \delta T \Big|_{V_i = V_{max}}$$

$$= \frac{55V - 24V}{L \cdot 50kHz} \cdot \frac{24V}{55V} < 1A$$

$$\Rightarrow L > 271 \mu H.$$



$$\hat{\phi} = \frac{V_i \delta T}{N_p}$$

$$\hat{\phi} = \frac{V_i}{N_p \delta} \cdot \frac{V_o}{V_i} < \phi_{max}$$

$$\Rightarrow N_p > \frac{V_o}{\phi_{max} \cdot \delta} = 18,045$$

$$\therefore \boxed{N_p = 19 \text{ vueltas}}$$

c) Peor caso: δ_{max} , V_{min}

$$D \cdot T \cdot \frac{V_i}{N_T} = \phi_{max} = \frac{V_i \delta T}{N_p} \Rightarrow D = \frac{N_T}{N_p} \cdot \delta < 1 - \delta$$

$$\Rightarrow N_T < \left(\frac{1}{\delta} - 1 \right) N_p = \left(\frac{45V}{24V} - 1 \right) \cdot 19 \text{ vueltas} = 16,625$$

$$\therefore \boxed{N_T = 16 \text{ vueltas}}$$

Problema 2 (cont.)

S.E. (5)

$$d) V_{Q\text{máx}} = V_{I\text{máx}} \left(1 + \frac{N_p}{N_T} \right) = 55V \left(1 + \frac{19}{16} \right) = 120,3 V$$

$$I_{Q\text{máx}} = \max_{V_i} \{ \hat{I}_S + \hat{I}_m \}$$

$$\hat{I}_m = \frac{V_i}{L_p} \cdot \Delta T = \frac{V_i}{L_{pf}} \frac{V_o}{V_i} = \frac{V_o}{L_{pf}} \quad \left\| \begin{array}{l} L_p = A_L N_p^2 \\ = 44,76 \mu\text{H} \end{array} \right.$$

$$\Rightarrow \hat{I}_m = 1,108 A \quad (\text{no depende de } V_i)$$

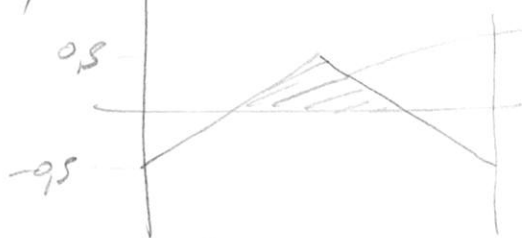
$$\hat{I}_S = I_{o\text{máx}} + \frac{1}{2} \Delta I_{L\text{máx}}$$

$$\text{Assumiendo } L = 271 \mu\text{H} \Rightarrow \Delta I_L = 1 A$$

$$\hat{I}_S = 5 A + 0,5 A = 5,5 A$$

$$\therefore I_{Q\text{máx}} = 6,608 A$$

e) $i_c(A)$



$$Q = \frac{\Delta I_L}{2} \cdot \frac{T}{2} \cdot \frac{1}{2} = \frac{1 A}{8f} = 2,5 \mu\text{C}$$

$$24V \cdot 0,02 = 480 \text{ mV}$$

→ Tomo factor de seguridad 2 → $V = 50 V$

→ Si sólo estuviera el efecto capacitivo necesitaría

$$C > \frac{2,5 \mu\text{C}}{480 \text{ mV}} = 5,2 \mu\text{F} \ll \text{a cualquier de los disponibles. Elijo el de } ESR = 0,401 \Omega \text{ de uso}$$

$$\text{que } \Delta V = 0,401 \Omega \cdot 1 A = 401 \text{ mV} < 480 \text{ mV.}$$

$$\text{Desprecio la componente capacitiva pues } \frac{2,5 \mu\text{C}}{560 \mu\text{F}} = 4 \text{ mV} \ll 401 \text{ mV}$$

$$\boxed{50V, 560 \mu\text{F}}$$