Preferred Device

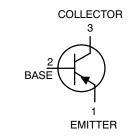
General Purpose Transistor

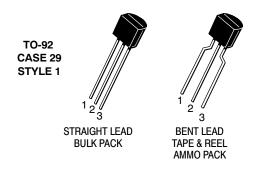
PNP Silicon



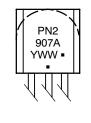
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



| PN2907A | = Device Code | |
|---------|-------------------|--|
| Y | = Year | |
| WW | = Work Week | |
| • | = Pb-Free Package | |

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

Features • These are Pb-Free Devices* **MAXIMUM RATINGS** Symbol Unit Rating Value Collector-Emitter Voltage -60 Vdc VCEO Collector-Base Voltage V_{CBO} -60 Vdc V_{EBO} Emitter-Base Voltage Vdc -5.0 Collector Current - Continuous -600 I_{C} mAdc Total Device Dissipation @ T_A = 25°C P_D 625 mW Derate above 25°C 5.0 mW/°C Total Device Dissipation @ T_C = 25°C w P_D 1.5

THERMAL CHARACTERISTICS

Operating and Storage Junction

Derate above 25°C

Temperature Range

| Characteristic | Symbol | Max | Unit |
|---|-----------------|------|------|
| Thermal Resistance, Junction- to- Ambient | $R_{\theta JA}$ | 200 | °C/W |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 83.3 | °C/W |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

*For additional information on our Pb-Free strategy and soldering details, please

download the ON Semiconductor Soldering and Mounting Techniques

Reference Manual, SOLDERRM/D.

mW/°C

°C

12

- 55 to +150

T_J, T_{sta}

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

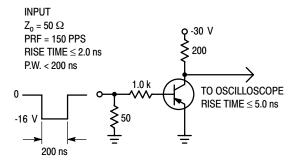
| Characteristic | | Symbol | Min | Max | Unit |
|--|---|----------------------|-------------------------------|-------------------------|------|
| OFF CHARACTERISTICS | | | | • | |
| Collector-Emitter Breakdown Voltage (Not $(I_C = -10 \text{ mAdc}, I_B = 0)$ | e 1) | V _{(BR)CEO} | -60 | - | Vdc |
| Collector-Base Breakdown Voltage (I _C = -10 µAdc, I _E = 0) | | V _{(BR)CBO} | -60 | - | Vdc |
| Emitter-Base Breakdown Voltage $(I_E = -10 \ \mu Adc, I_C = 0)$ | | V _{(BR)EBO} | -5.0 | - | Vdc |
| Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc) | | ICEX | - | -50 | nAdc |
| Collector Cutoff Current $(V_{CB} = -50 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -50 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C})$ | | I _{CBO} | - | -0.01 -10 | μAdc |
| Base Current (V _{CE} = -30 Vdc, V _{EB(off)} = -0.5 Vdc) | | I _B | - | -50 | nAdc |
| ON CHARACTERISTICS | | | | | • |
| $\begin{array}{l} \text{DC Current Gain} \\ (I_{C} = -0.1 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \\ (I_{C} = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \\ (I_{C} = -10 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \\ (I_{C} = -150 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \\ (I_{C} = -500 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}) \\ (\text{Note}) \\ ($ | | h _{FE} | 75 100 100 100 50 | - - - 300 - | - |
| Collector-Emitter Saturation Voltage (Note 1) ($I_C = -150 \text{ mAdc}$, $I_B = -15 \text{ mAdc}$) ($I_C = -500 \text{ mAdc}$, $I_B = -50 \text{ mAdc}$) | | V _{CE(sat)} | - | -0.4 -1.6 | Vdc |
| Base-Emitter Saturation Voltage (Note 1) (I_C = -150 mAdc, I_B = -15 mAdc) (I_C = -500 mAdc, I_B = -50 mAdc) | | V _{BE(sat)} | - | -1.3 -2.6 | Vdc |
| SMALL-SIGNAL CHARACTERISTICS | | • | | 1 | 1 |
| Current-Gain - Bandwidth Product (Notes 1 and 2), (I _C = -50 mAdc, V _{CE} = -20 Vdc, f = 100 MHz) | | fT | 200 | - | MHz |
| Output Capacitance (V _{CB} = -10 Vdc, I _E = 0, f = 1.0 MHz) | | C _{obo} | - | 8.0 | pF |
| Input Capacitance (V _{EB} = -2.0 Vdc, I _C = 0, f = 1.0 MHz) | | C _{ibo} | - | 30 | pF |
| SWITCHING CHARACTERISTICS | | | | • | • |
| Turn-On Time | (V _{CC} = -30 Vdc, I _C = -150 mAdc, I _{B1} = -15 mAdc) (Figures 1 and 5) | t _{on} | - | 45 | ns |
| Delay Time | | t _d | - | 10 | ns |
| Rise Time | | t _r | - | 40 | ns |
| Turn-Off Time | $(V_{CC} = -6.0 \text{ Vdc}, I_C = -150 \text{ mAdc}, I_{B1} = I_{B2} = 15 \text{ mAdc})$ (Figure 2) | t _{off} | - | 100 | ns |
| Storage Time | | t _s | - | 80 | ns |
| Fall Time | | t _f | - | 30 | ns |

1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2%. 2. f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|--------------|--------------------|-----------------------|
| PN2907AG | TO-92 (Pb-Free) | 5000 Units / Bulk |
| PN2907ARLRAG | TO-92 (Pb-Free) | 2000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.





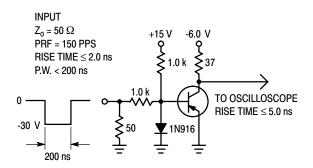


Figure 2. Storage and Fall Time Test Circuit

TYPICAL CHARACTERISTICS

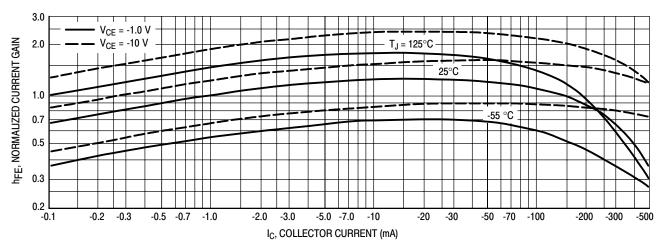
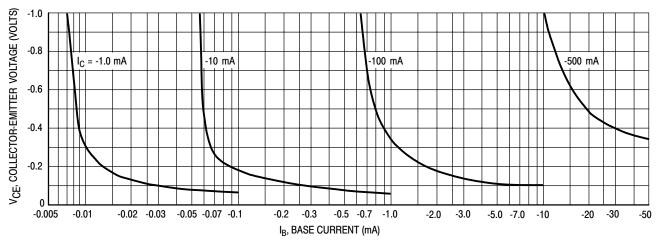
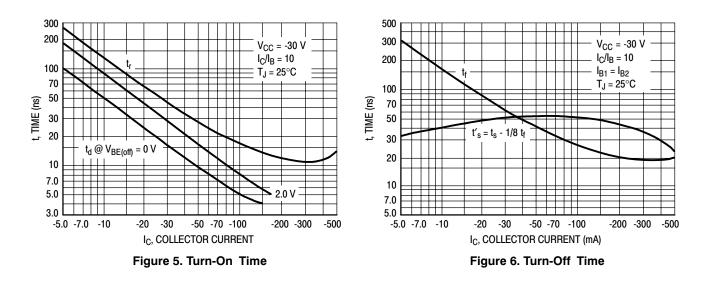
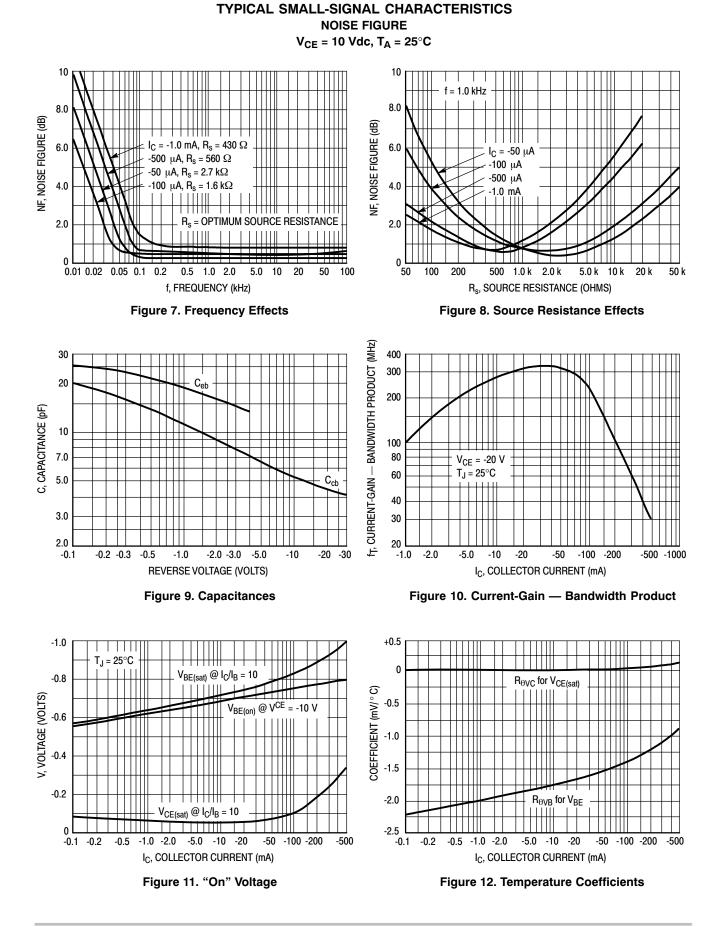


Figure 3. DC Current Gain



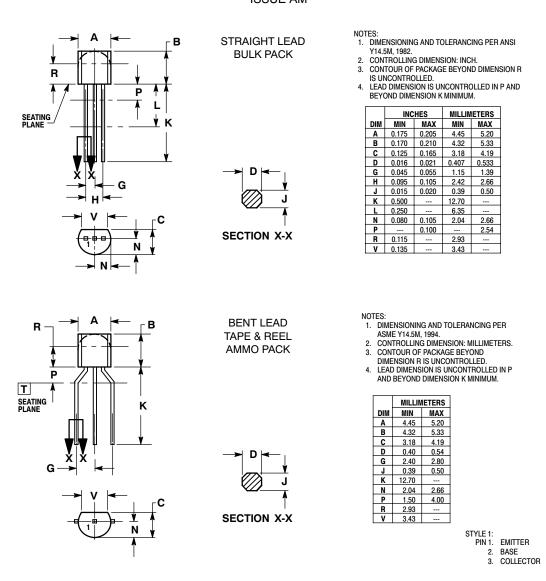






PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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